

XX XX TTTTTTTTTT NN NN TTTTTTTTTT AAAAAA BBBBBBBB
XX XX TTTTTTTTTT NN NN TTTTTTTTTT AAAAAA BBBBBBBB
XX XX TT NN NN TT AA AA BB BB
XX XX TT NNNN NN NN TT AA AA BB BB
XX XX TT NN NN TT AA AA BBBBBBBB
XX XX TT NN NNNN NN NN TT AA AA BBBBBBBB
XX XX TT NN NNNN NN NN TT AA AA BBBBBBBB
XX XX TT NN NN TT AA AA BB BB
XX XX TT NN NN TT AA AA BB BB
XX XX TT NN NN TT AA AA BBBBBBBB
XX XX TT NN NN TT AA AA BBBBBBBB

....

RRRRRRRR EEEEEEEEEE QQQQQQ
RRRRRRRR EEEEEEEEEE QQQQQQ
RR RR EE QQ QQ
RR RR EE QQ QQ
RR RR EE QQ QQ
RR RR EEEE QQ QQ
RR RR EEEE QQ QQ
RR RR EE QQ QQ
RR RR EE QQ QQ
RR RR EEEE QQ QQ
RR RR EEEE QQ QQ

Version: 'V04-000'

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FACILITY: DSR (Digital Standard RUNOFF) / DSRPLUS

ABSTRACT:
Parallel tables for associating index entries and pages.

NOTE: The tables contain one extra entry, which is unused.
That is so subtraction of 1 can be forgotten about.

ENVIRONMENT: Transportable BLISS

AUTHOR: Rich Friday

CREATION DATE: 1978

MODIFIED BY:

002 KAD00002 Keith Dawson 07-Mar-1983
Global edit of all modules. Updated module names, idents,
copyright dates. Changed require files to BLISS library.

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LITERAL

max_xtn_count = 100, !Maximum number of transaction numbers (condensed).

!Number of BLISS values in a set of pages.

xtn_pagtab_size = (max_xtn_count + 1) * page_sct_size,

!Number of BLISS values in a list of transaction numbers.

! xtn_xtntab_size = max_xtn_count + 1;

MACRO

 xtntab_define = VECTOR [xtn_xtntab_size] %;

 xpagen_define = BLOCKVECTOR [max_xtn_count + 1, page_sct_size] %;

!

End of XTNTAB.REQ

